



Linguaggi di Programmazione

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Applicazioni del CCS

CCS: sintassi

| | | | |
|--------|-------|----------------------|---|
| p, q | $::=$ | nil | processo inattivo |
| | | x | variabile di processo (per la ricorsione) |
| | | $\mu.p$ | prefisso azione |
| | | $p \setminus \alpha$ | canale ristretto |
| | | $p[\phi]$ | rietichettatura del canale |
| | | $p + q$ | scelta nondeterministica (somma) |
| | | $p q$ | composizione parallela |
| | | rec $x. p$ | ricorsione |

(gli operatori sono elencati in ordine di precedenza)

Un po' di notazione

scriviamo $\sum_{i=1}^n p_i$ invece di $p_1 + \cdots + p_n$

scriviamo $\prod_{i=1}^n p_i$ invece di $p_1 | \cdots | p_n$

scriviamo $p \setminus \{a_1, \dots, a_n\}$ invece di $p \setminus a_1 \cdots \setminus a_n$

scriviamo $\mu^n . p$ invece di $\underbrace{\mu . \mu \dots \mu}_{n} . p$

CCS op. semantics

$$\text{Act) } \frac{}{\mu.p \xrightarrow{\mu} p} \quad \text{Res) } \frac{p \xrightarrow{\mu} q \quad \mu \notin \{\alpha, \bar{\alpha}\}}{p \setminus \alpha \xrightarrow{\mu} q \setminus \alpha} \quad \text{Rel) } \frac{p \xrightarrow{\mu} q}{p[\phi] \xrightarrow{\phi(\mu)} q[\phi]}$$

$$\text{SumL) } \frac{p_1 \xrightarrow{\mu} q}{p_1 + p_2 \xrightarrow{\mu} q} \quad \text{SumR) } \frac{p_2 \xrightarrow{\mu} q}{p_1 + p_2 \xrightarrow{\mu} q}$$

$$\text{ParL) } \frac{p_1 \xrightarrow{\mu} q_1}{p_1 | p_2 \xrightarrow{\mu} q_1 | p_2} \quad \text{Com) } \frac{p_1 \xrightarrow{\lambda} q_1 \quad p_2 \xrightarrow{\bar{\lambda}} q_2}{p_1 | p_2 \xrightarrow{\tau} q_1 | q_2} \quad \text{ParR) } \frac{p_2 \xrightarrow{\mu} q_2}{p_1 | p_2 \xrightarrow{\mu} p_1 | q_2}$$

$$\text{Rec) } \frac{p[\mathbf{rec} \ x. \ p / x] \xrightarrow{\mu} q}{\mathbf{rec} \ x. \ p \xrightarrow{\mu} q}$$

CCS

Codificare un linguaggio imperativo

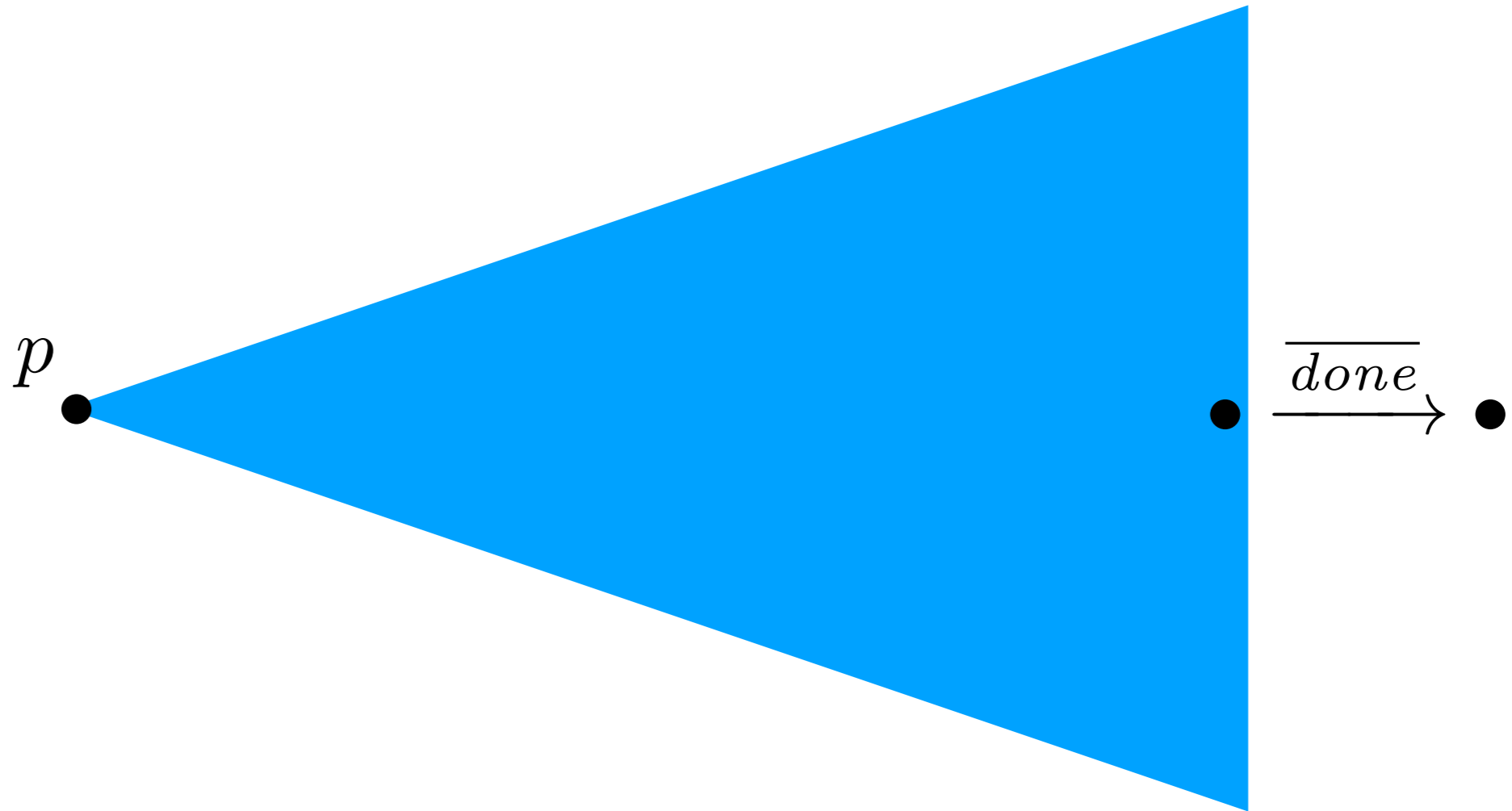
Prima cosa: terminazione

Un canale dedicato *done*:

viene inviato un messaggio quando il comando corrente termina

$$\text{Done} \triangleq \overline{\text{done}}$$
$$\text{Done} \xrightarrow{\overline{\text{done}}} \mathbf{nil}$$

Terminazione



Skip

skip

non fa nulla invia *done*

τ .Done

● $\xrightarrow{\tau}$ Done $\xrightarrow{\overline{done}}$ **nil**

Variabili

x che varia su $V = \{v_1, \dots, v_n\}$

un processo dedicato alla gestione di ogni variabile

possiamo leggere il suo valore attuale (xr_i canale)

possiamo scrivere qualsiasi valore (canale xw_i)

$$\begin{aligned} XW &\triangleq \sum_{i=1}^n xw_i \cdot X_i \\ &= xw_1 \cdot X_1 + \dots + xw_n \cdot X_n \end{aligned}$$

$$X_i \triangleq \overline{xr}_i \cdot X_i + XW$$

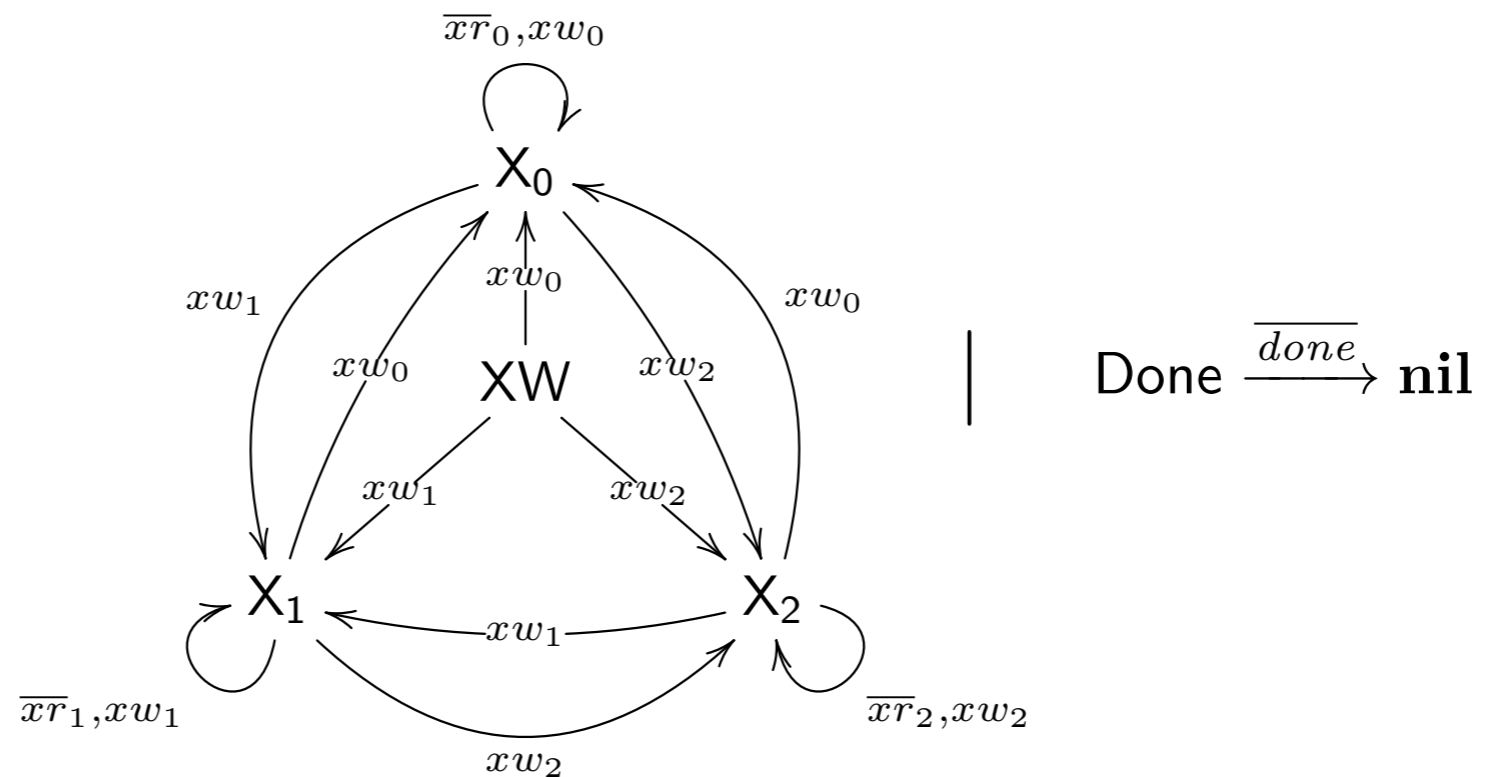
Dichiarazione di variabili

`var x`

rilascia una variabile non inizializzata e termina

$XW | Done$

$$V = \{v_0, v_1, v_2\}$$



Assignamento

$$x := v_i$$

invia un messaggio per cambiare lo stato della variabile e poi termina

$$\overline{xw_i}.Done$$

- $\overline{xw_i} \rightarrow Done \xrightarrow{\overline{done}} \mathbf{nil}$

Composizione sequenziale

$c_1 ; c_2$

assumiamo p_1 modella c_1
 p_2 modella c_2

$p_1 | done.p_2$

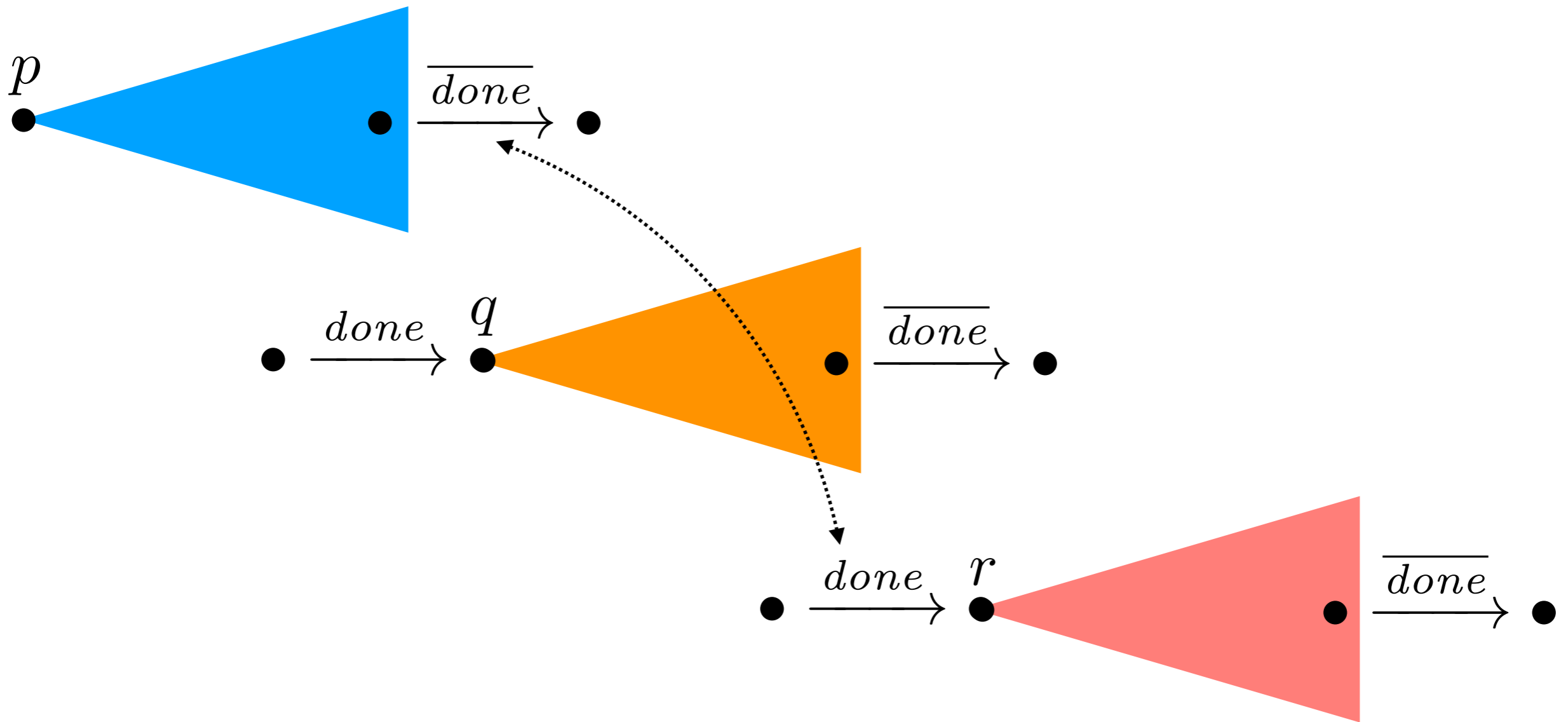
scelta infelice: non è scalabile

$c_1 ; c_2 ; c_3$

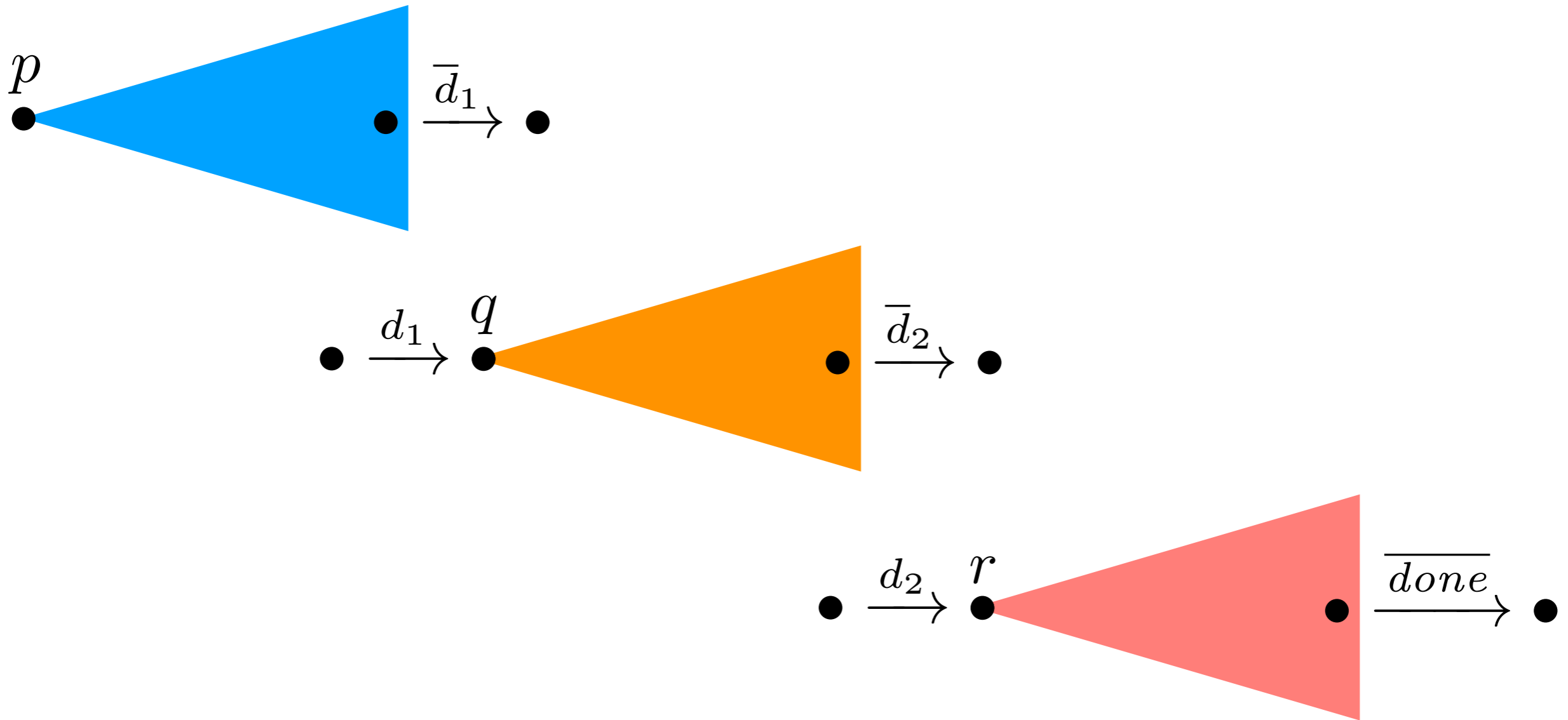
$p_1 | done.p_2 | done.p_3$

p_3 puo' iniziare dopo p_1

Sequenziale?



Sequential!



Composizione sequenziale

$$c_1 ; c_2$$

assumiamo p_1 modella c_1 $\phi_d(done) = d$
 p_2 modella c_2

$$p_1 \curvearrowright p_2 \triangleq (p_1[\phi_d] | d.p_2) \setminus d$$

ora d e' locale a p_1 e p_2

$$((p_1[\phi_{d_1}] | d_1.p_2) \setminus d_1)[\phi_{d_2}] | d_2.p_3) \setminus d_2$$

$$((p_1[\phi_d] | d.p_2) \setminus d)[\phi_d] | d.p_3) \setminus d$$

$$(p_1 \curvearrowright p_2) \curvearrowright p_3$$

Condizionale

if $x = v_i$ then c_1 else c_2

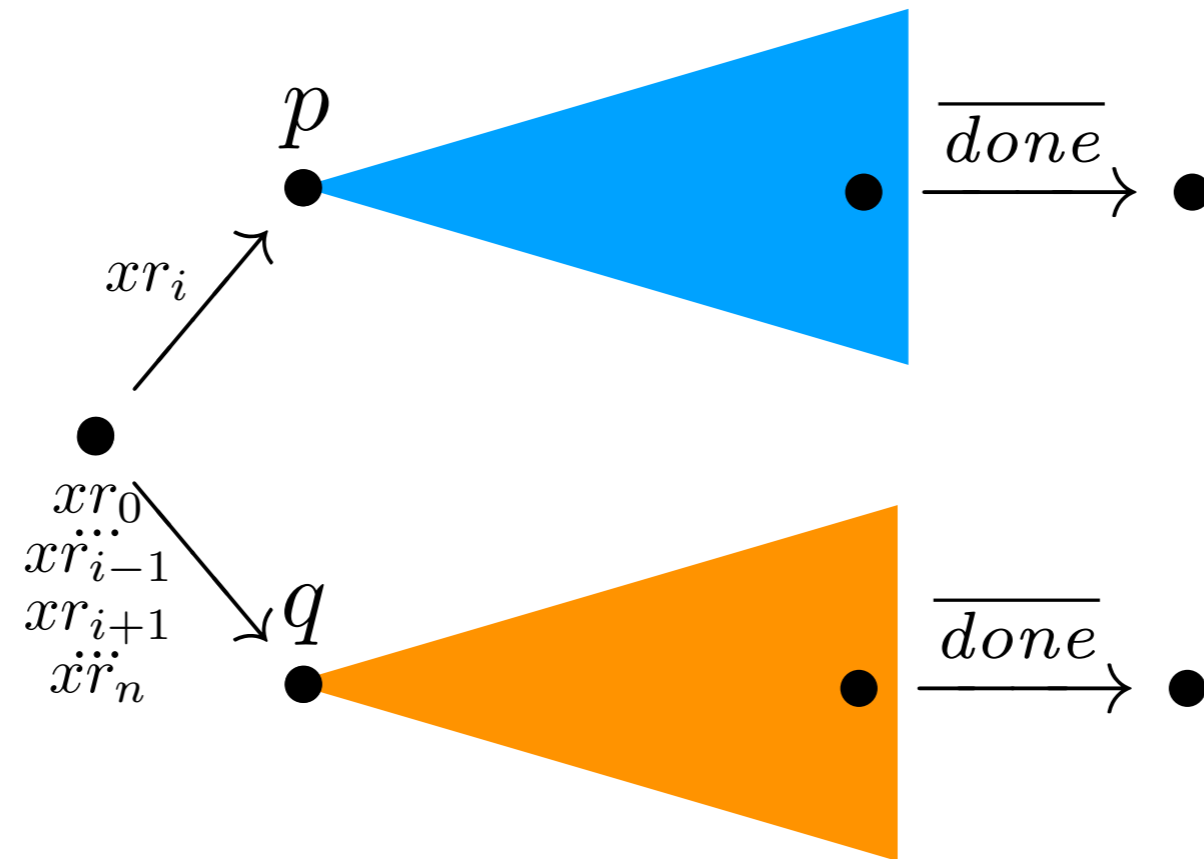
assumiamo p_1 modella c_1

p_2 modella c_2

riceve lo stato della variabile
e poi sceglie di conseguenza

$$x r_i \cdot p_1 + \sum_{j \neq i} x r_j \cdot p_2$$

Condizionale



Iterazione

while $x = v_i$ do c

assumiamo p modella c

riceve lo stato della variabile

e poi sceglie di conseguenza, eventualmente ricorrendo

$$\mathbf{rec} \ y. \ x r_i. (p[\phi_d] | d.y) \setminus d + \sum_{j \neq i} x r_j. \mathbf{Done}$$

$$Y \triangleq x r_i. (p[\phi_d] | d.Y) \setminus d + \sum_{j \neq i} x r_j. \mathbf{Done}$$

$$Y \triangleq x r_i. (p \frown Y) + \sum_{j \neq i} x r_j. \mathbf{Done}$$

Riassumendo

tutti i canali di comunicazione con le variabili
devono essere ristretti
per garantire che le richieste di lettura/scrittura siano
sincronizzate

$$p \setminus \{xw_1, xr_1, \dots\}$$

sono possibili diverse ottimizzazioni:
prefisso d'azione invece del collegamento per la composizione
sequenziale
guardie più espressive
rimuovere le transizioni silenziose
introdurre costanti per i cicli
implementare espressioni

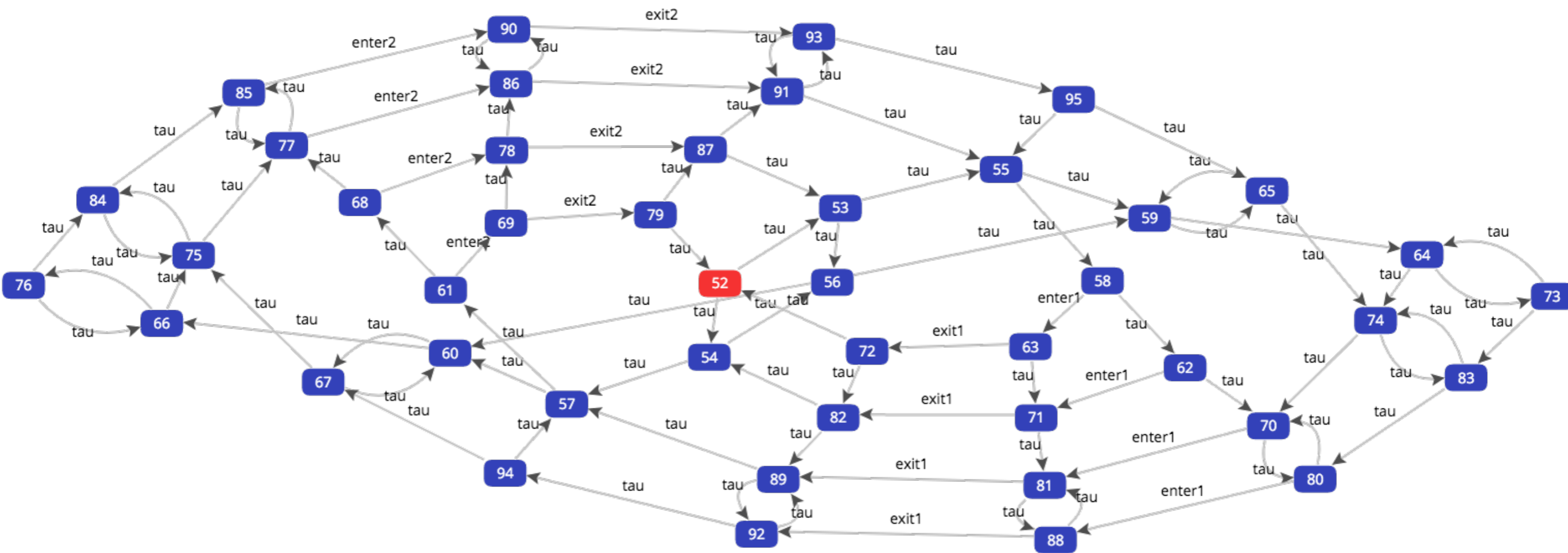
Example: optimisation

$$x := 1; y := 2$$
$$\overline{xw_1.done} \quad \frown \quad \overline{yw_2.done}$$
$$((\overline{xw_1.done})[\phi_d] \mid d.\overline{yw_2.done}) \setminus d$$
$$\overline{xw_1.yw_2.done}$$

CCS

Giochiamo con CAAL

Concurrency Workbench, Aalborg Edition



<http://caal.cs.aau.dk/>

CAAL

CAAL è uno strumento basato sul web per la modellazione, la visualizzazione e la verifica di processi concorrenti in CCS (e la sua estensione temporale) sviluppato per scopi educativi

Edit permette di specificare processi (definiti ricorsivamente)

Explore i loro LTS (collassando stati equivalenti)

Verify controlla la soddisfazione delle formule HML (model checking)

Verify equivalenza tra processi

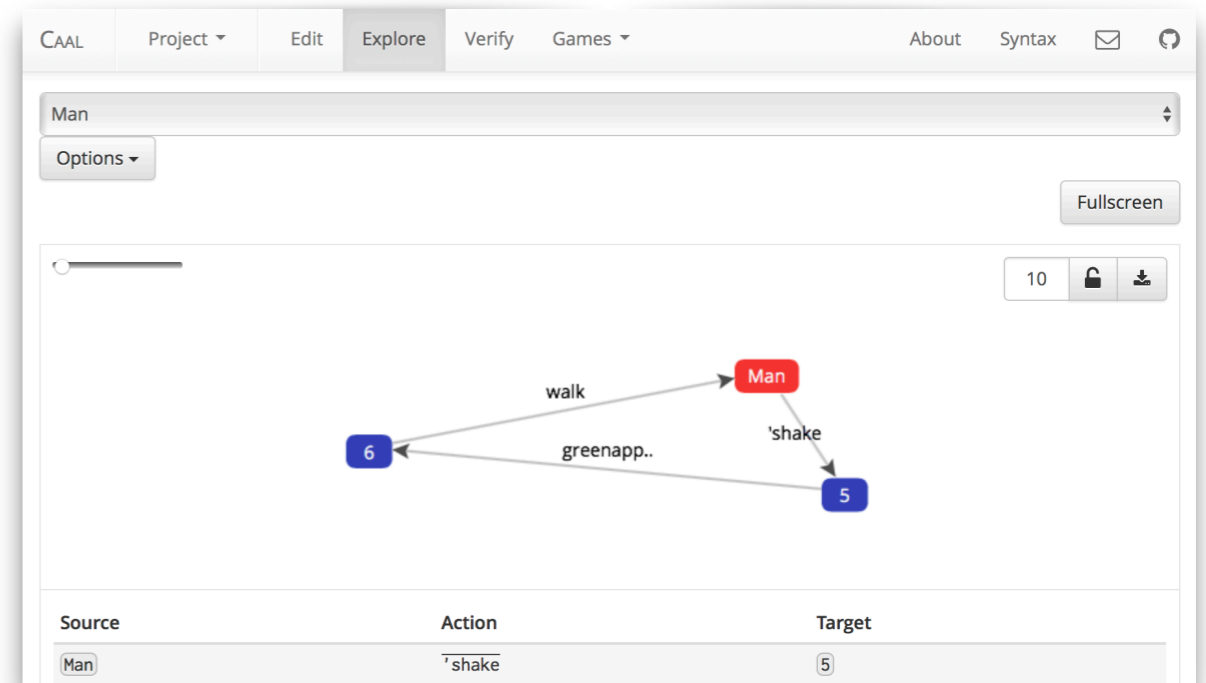
permette di giocare al **Game** della simulazione

CAAL

Edit processes

```
CAAL Project Edit Explore Verify Games About Syntax
Orchard
Parse CCS TCCS 20
1 Man = 'shake.(redapple.walk.Man + greenapple.walk.Man);
2
3 AppleTree = shake.('greenapple.AppleTree + 'redapple.AppleTree);
4
5 Orchard = (AppleTree | Man) \ {shake, redapple, greenapple};
6
7 Spec = walk.Spec;
```

Explore LTS



Verify HML & Equivalences

| Status | Time | Property | Verify | Edit | Delete | Options |
|--------|-------|--|--------|------|--------|---------|
| ✖ | 26 ms | Orchard ~ Spec | ▶ | ✎ | 🗑️ | ☰ |
| ✔ | 25 ms | Orchard \models $\langle \text{tau} \rangle \text{tt}$ | ▶ | ✎ | 🗑️ | ☰ |
| ✖ | 25 ms | Spec \models $\langle \text{tau} \rangle \text{tt}$ | ▶ | ✎ | 🗑️ | ☰ |
| ✔ | 25 ms | Orchard \approx Spec | ▶ | ✎ | 🗑️ | ☰ |

play bisimulation Game

CAAL sintassi per CCS

nil 0

a.P $\bar{a}.P$ $\tau.P$ a.P 'a.P tau.P

P + Q P + Q

P | Q P | Q

$P \setminus \{a_1, \dots, a_n\}$ $P \setminus \{a_1, \dots, a_n\}$

$P \left[\begin{smallmatrix} b_1 \\ \hline \end{smallmatrix} / a_1, \dots, \begin{smallmatrix} b_n \\ \hline \end{smallmatrix} / a_n \right]$ $P[b_1/a_1, \dots, b_n/a_n]$

$A \triangleq P$ $A = P ;$

CAAL sintassi per HML

tt tt

ff ff

$F \wedge G$ F and G

$F \vee G$ F or G

$\diamond_{\{a_1, \dots, a_n\}} F$ $\langle a_1, \dots, a_n \rangle F$

$\square_{\{a_1, \dots, a_n\}} F$ $[a_1, \dots, a_n] F$

CAAL

mutual exclusion protocols analysis

Peterson's mex algorithm

```
% Two processes P1, P2
% Two boolean variables b1, b2 (both initially false)
% when Pi wants to enter the critical section, then it sets bi to true
% An integer variable k, taking values in {1,2}
% (initial value is arbitrary)
% the process Pk has priority over the other process
%
% Process P1 in pseudocode
while (true) {
    ...                               % non critical section
    b1 = true ;                       % P1 wants to enter the critical section
    k = 2 ;                           % P1 gives priority to the other process
    while (b2 && k==2) skip ;         % P1 waits its turn
    ...                               % P1 enters the critical section
    b1 = false                        % P1 leaves the critical section
}

% Process P2 is analogous to P1
```

Peterson's mex in CCS

$$B1W \triangleq b1wf.B1f + b1wt.B1t$$

$$B1f \triangleq \overline{b1rf}.B1f + B1W$$

$$B1t \triangleq \overline{b1rt}.B1t + B1W$$

$$KW \triangleq kw1.K1 + kw2.K2$$

$$K1 \triangleq \overline{kr1}.K1 + KW$$

$$K2 \triangleq \overline{kr2}.K2 + KW$$

% Process P1 in pseudocode

```
while (true) {
  ...
  b1 = true ; % wants to enter
  k = 2 ; % gives priority to P2
  while (b2 && k==2) skip ; % waits
  ... % enters critical section
  b1 = false % leaves
}
```

% Process P2 is analogous to P1

$$P1 \triangleq \overline{b1wt}.\overline{kw2}.P1a$$

$$P1a \triangleq b2rf.P1b + b2rt.(kr1.P1b + kr2.P1a)$$

leave cycle
loop

$$P1b \triangleq enter1.exit1.\overline{b1wf}.P1$$

just something to observe

$$SP \triangleq (B1f|B2f|KW|P1|P2) \setminus \{kw1, kr1, kw2, kr2, \dots\}$$

Peterson's mex in CCS

$$B1W \triangleq b1wf.B1f + b1wt.B1t$$

$$B1f \triangleq \overline{b1rf}.B1f + B1W$$

$$B1t \triangleq \overline{b1rt}.B1t + B1W$$

$$KW \triangleq kw1.K1 + kw2.K2$$

$$K1 \triangleq \overline{kr1}.K1 + KW$$

$$K2 \triangleq \overline{kr2}.K2 + KW$$

`% Process P1 in pseudocode`

`while (true) {`

`...`

`b1 = true ; % wants to enter`

`k = 2 ; % gives priority to P2`

`while (b2 && k==2) skip ; % waits`

`... % enters critical section`

`b1 = false % leaves`

`}`

$$P1 \triangleq \overline{b1wt}.\overline{kw2}.P1a$$

$$P1a \triangleq b2rf.P1b + \overbrace{kr1.P1b}^{\text{no busy wait}}$$

$$P1b \triangleq enter1.exit1.\overline{b1wf}.P1$$

`% Process P2 is analogous to P1`

$$SP \triangleq (B1f|B2f|KW|P1|P2) \setminus \{kw1, kr1, kw2, kr2, \dots\}$$

Peterson's mex in CCS

$$P1 \triangleq \overline{b1wt}.\overline{kw2}.P1a$$

$$P1a \triangleq b2rf.P1b + kr1.P1b$$

$$P1b \triangleq enter1.exit1.\overline{b1wf}.P1$$

$$SP \triangleq (B1f|B2f|KW|P1|P2) \setminus \{kw1, kr1, kw2, kr2, \dots\}$$

una formula per la mutua esclusione? qualsiasi label

$$F \triangleq [exit_1]\mathbf{ff} \vee [exit_2]\mathbf{ff}$$

$$F \wedge [-](\dots)$$

$$F \wedge [-](F \wedge [-](\dots))$$

$$MEX \triangleq_{(max)} F \wedge [-]MEX$$

$$F \wedge [-](F \wedge [-](F \wedge [-](\dots)))$$

una formula definita ricorsivamente!

Peterson's mex in CCS

$$P1 \triangleq \overline{b1wt}.\overline{kw2}.P1a$$

$$P1a \triangleq b2rf.P1b + kr1.P1b$$

$$P1b \triangleq enter1.exit1.\overline{b1wf}.P1$$

$$SP \triangleq (B1f|B2f|KW|P1|P2) \setminus \{kw1, kr1, kw2, kr2, \dots\}$$

P1 ha la possibilita' di entrare?

$$G \triangleq \langle enter_1 \rangle \mathbf{tt}$$

$$G \vee \langle - \rangle (\dots)$$

$$G \vee \langle - \rangle (G \vee \langle - \rangle (\dots))$$

$$EN_{(\min)} \triangleq G \vee \langle - \rangle EN$$

$$G \vee \langle - \rangle (G \vee \langle - \rangle (G \vee \langle - \rangle (\dots)))$$

una formula definita ricorsivamente!

Peterson's mex in CCS

$$P1 \triangleq \overline{b1wt}. \overline{kw2}. P1a$$

$$P1a \triangleq b2rf.P1b + kr1.P1b$$

$$P1b \triangleq enter1.exit1.\overline{b1wf}.P1$$

$$SP \triangleq (B1f|B2f|KW|P1|P2) \setminus \{kw1, kr1, kw2, kr2, \dots\}$$

non abbiamo deadlock?

$$H \triangleq \langle - \rangle \mathbf{tt}$$

$$H \wedge [-](\dots)$$

$$H \wedge [-](H \wedge [-](\dots))$$

$$DF_{(max)} \triangleq H \wedge [-]DF$$

$$H \wedge [-](H \wedge [-](H \wedge [-](\dots)))$$

una formula definita ricorsivamente!

Peterson's mex in CCS

$$P1 \triangleq \overline{b1wt}.req_1.\overline{kw2}.P1a$$

$$P1a \triangleq b2rf.P1b + kr1.P1b$$

$$P1b \triangleq enter1.exit1.\overline{b1wf}.P1$$

$$SP \triangleq (B1f|B2f|KW|P1|P2) \setminus \{kw1, kr1, kw2, kr2, \dots\}$$

P1 puo' entrare ogni volta che fa una richiesta?

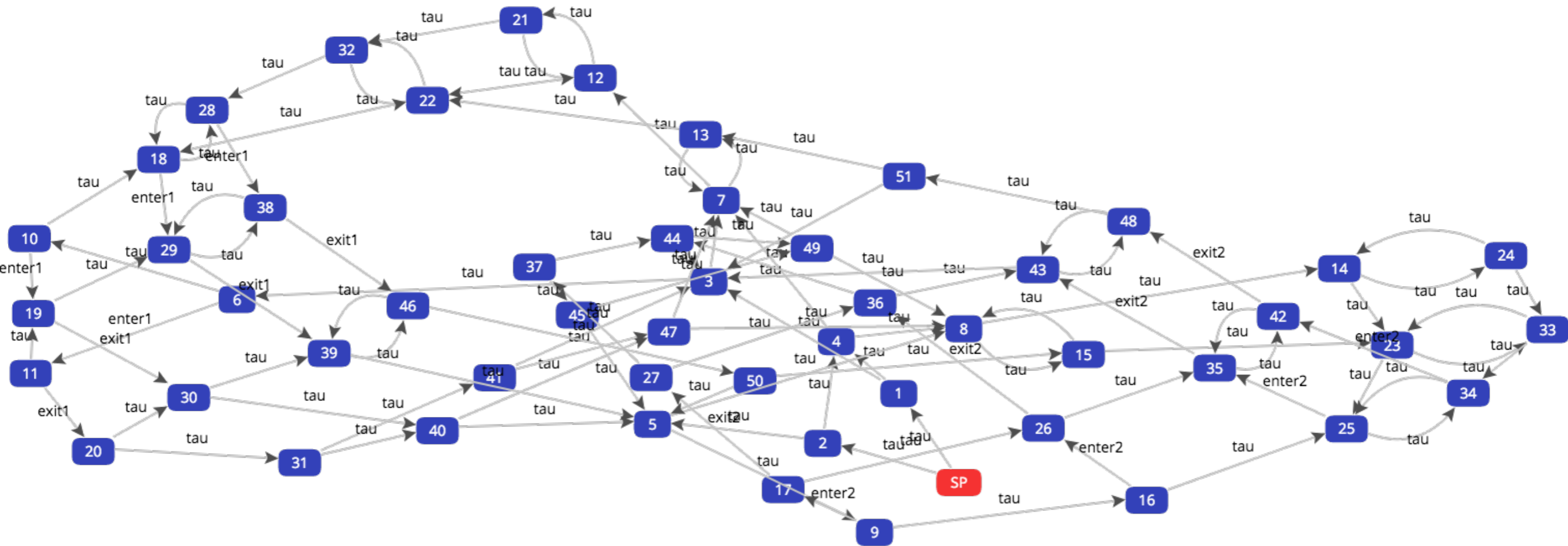
$$A \triangleq_{(min)} \langle exit_1 \rangle \mathbf{tt} \vee [-]A$$

$$REQ \triangleq_{(max)} [req_1]A \wedge [-]REQ$$

una formula definita ricorsivamente!

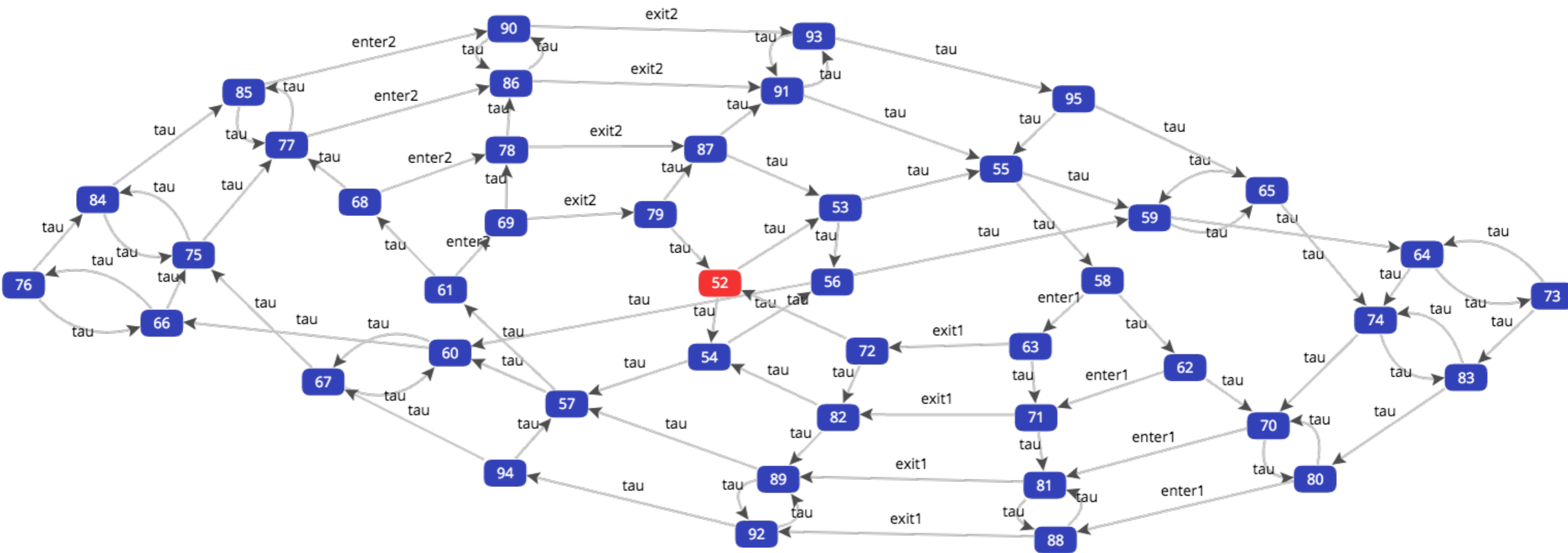
Peterson's mex in CAAL

LTS



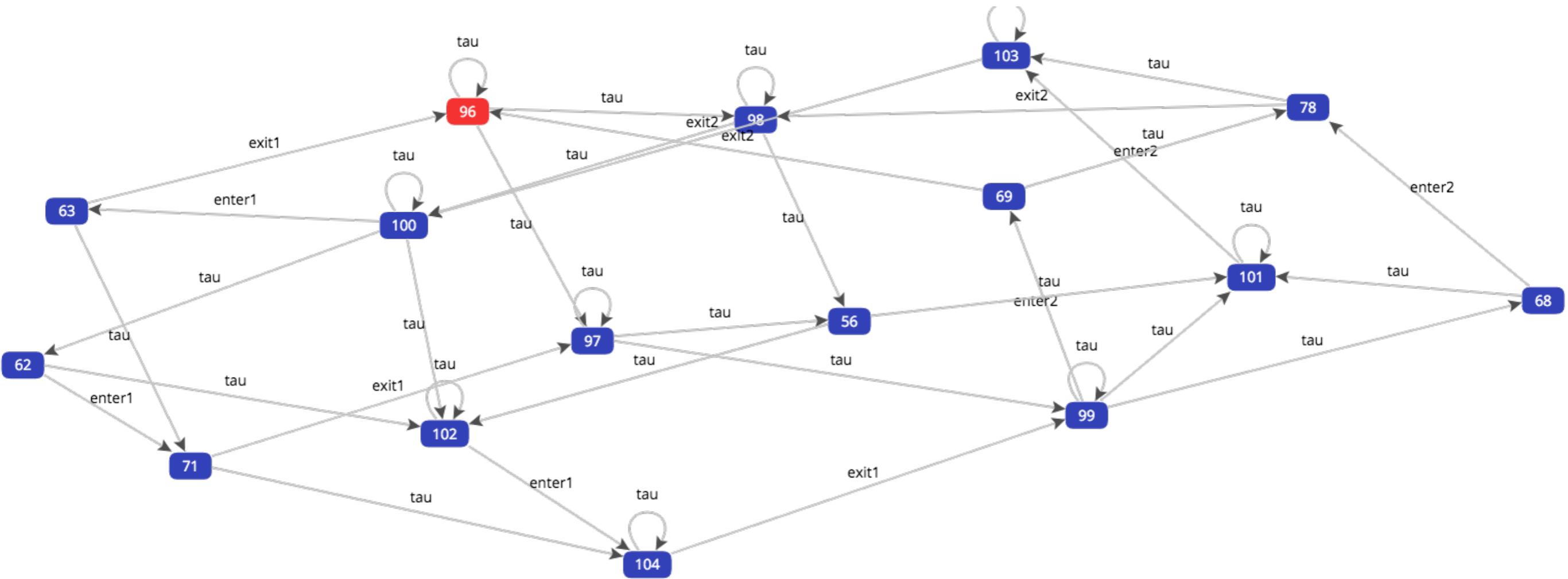
Peterson's mex in CAAL

LTS up to strong bisimilarity



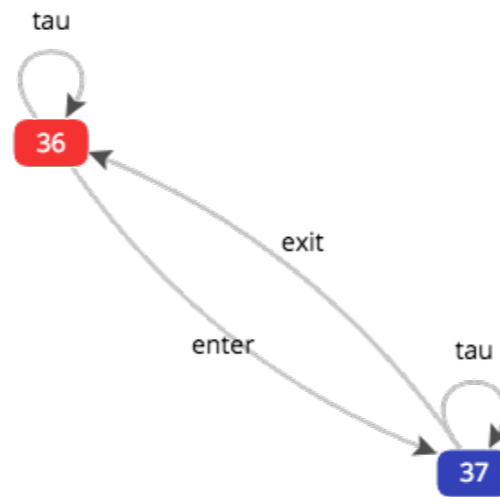
Peterson's mex in CAAL

LTS up to weak bisimilarity



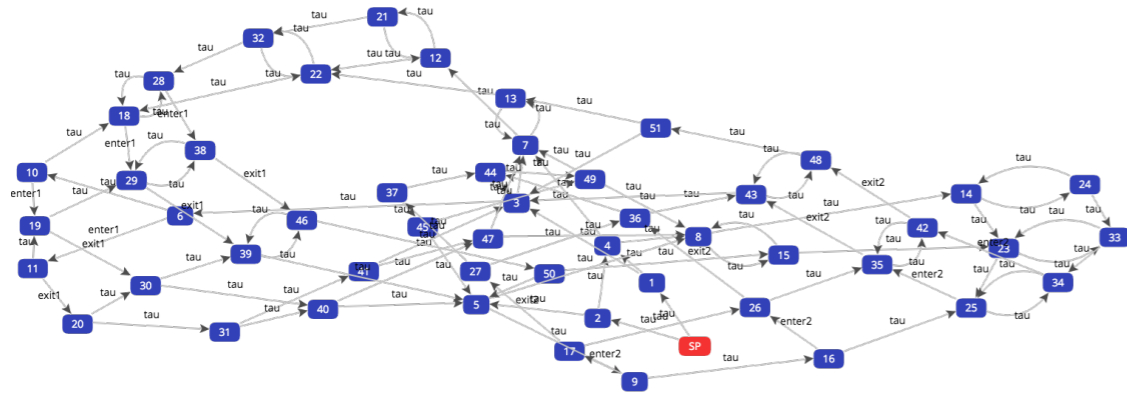
Peterson's mex in CAAL

LTS up to weak bisimilarity, after renaming *enter1/2* to *enter* and *exit1/2* to *exit*

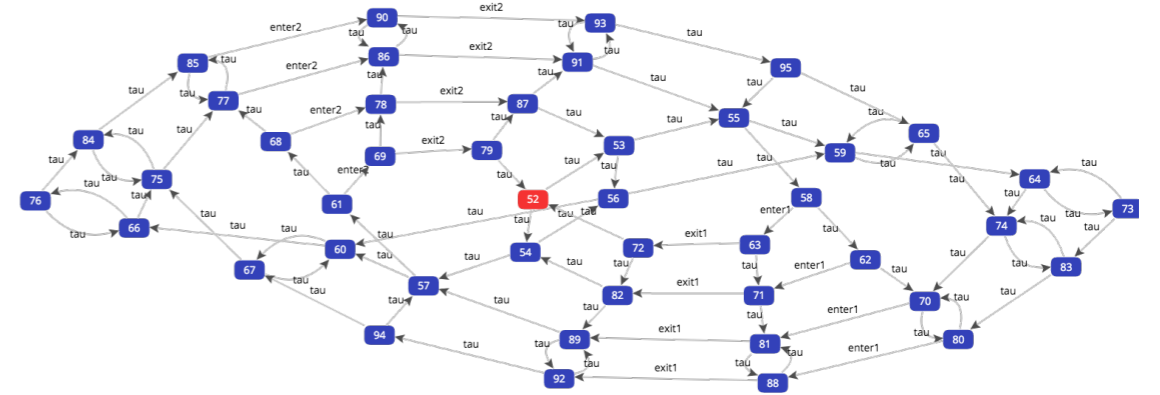


Peterson's mex in CAAL

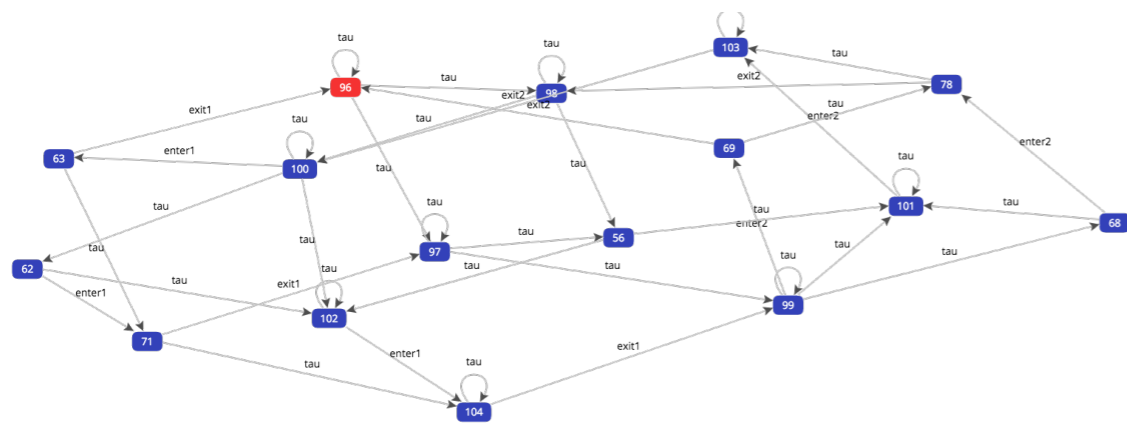
LTS



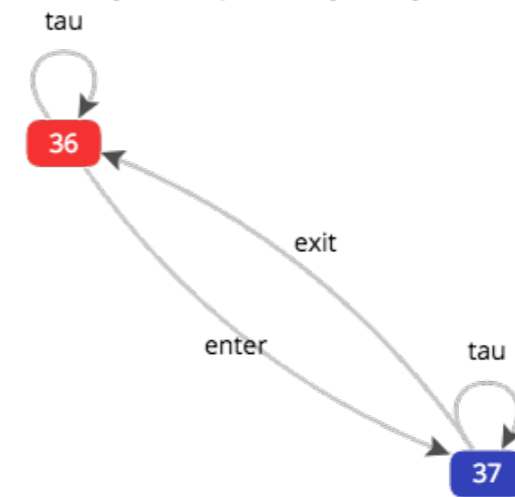
LTS up to strong bisimilarity



LTS up to weak bisimilarity



LTS up to weak bisimilarity,
enter and exit



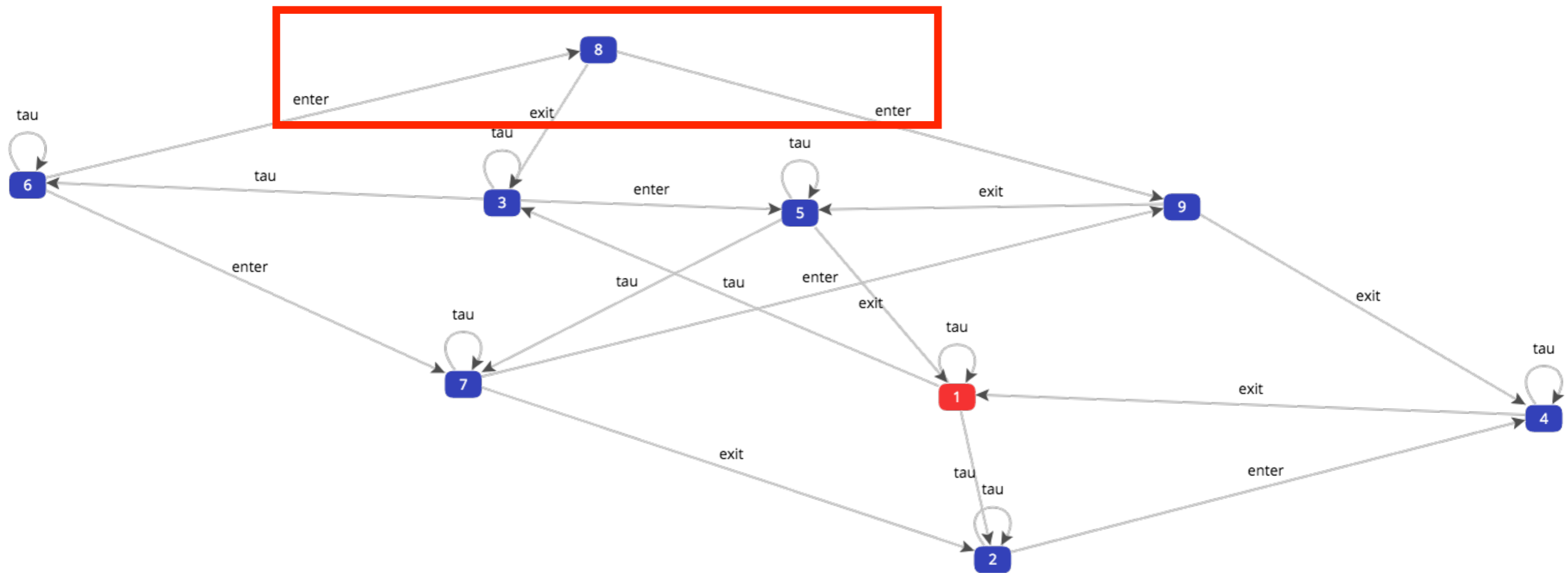
Hymman's mex algorithm

```
% Two processes H1, H2
% Two boolean variables b1, b2 (both initially false)
% when Hi wants to enter the critical section, then it sets bi to true
% An integer variable k, taking values in {1,2}
% (initial value is arbitrary)
% the process Hk has priority over the other process
%
% Process H1 in pseudocode
while (true) {
    ...                               % non critical section
    b1 = true ;                       % H1 wants to enter the critical section
    while (k==2) {                   % while H2 has priority
        while (b2) skip ;           % H1 waits
        k = 1;                       % H1 sets priority to itself
    }
    ...                               % H1 enters the critical section
    b1 = false                       % H1 leaves the critical section
}

% Process H2 is analogous to H1
```


Peterson's mex in CAAL

LTS up to weak bisimilarity, after renaming *enter1/2* to *enter* and *exit1/2* to *exit*



50 prisoners puzzle

50 prisoners kept in separate cells got a chance to be released:
from time to time one of them will be carried in a special room and then back to cell.
(in no particular order, possibly multiple times consecutively, but fairly to avoid infinite wait)

The room is completely empty except for a switch that can turn the light on or off
(the light is not visible from outside).

At any time, if any of them claims rightfully that all the prisoners have already entered the
room at least once, then all prisoners will be released
(but if it proves wrong, then the chance ends and they will never be released).

The prisoners have the possibility to discuss in advance some protocol to follow
(not all prisoner must behave in the same way).

Can you find a winning strategy for the prisoners?
Can you formalise it in CCS (for 2 to 4 prisoners)?

- Easy case: it is known that the light in the room is initially off
- Hard case: the initial state of the light in the room is not known.