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**PSC 2020/21** (375AA, 9CFU)

Principles for Software Composition

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18a - CCS abstract semantics

# CCS

## Graph isomorphism

# CCS syntax

$p, q$	$::=$	<b>nil</b>	inactive process
		$x$	process variable (for recursion)
		$\mu.p$	action prefix
		$p \setminus \alpha$	restricted channel
		$p[\phi]$	channel relabelling
		$p + q$	nondeterministic choice (sum)
		$p   q$	parallel composition
		<b>rec</b> $x. p$	recursion

(operators are listed in order of precedence)

# CCS op. semantics

$$\begin{array}{c}
 \text{Act)} \frac{}{\mu.p \xrightarrow{\mu} p} \qquad \text{Res)} \frac{p \xrightarrow{\mu} q \quad \mu \notin \{\alpha, \bar{\alpha}\}}{p \setminus \alpha \xrightarrow{\mu} q \setminus \alpha} \qquad \text{Rel)} \frac{p \xrightarrow{\mu} q}{p[\phi] \xrightarrow{\phi(\mu)} q[\phi]} \\
 \\
 \text{SumL)} \frac{p_1 \xrightarrow{\mu} q}{p_1 + p_2 \xrightarrow{\mu} q} \qquad \text{SumR)} \frac{p_2 \xrightarrow{\mu} q}{p_1 + p_2 \xrightarrow{\mu} q} \\
 \\
 \text{ParL)} \frac{p_1 \xrightarrow{\mu} q_1}{p_1 | p_2 \xrightarrow{\mu} q_1 | p_2} \qquad \text{Com)} \frac{p_1 \xrightarrow{\lambda} q_1 \quad p_2 \xrightarrow{\bar{\lambda}} q_2}{p_1 | p_2 \xrightarrow{\tau} q_1 | q_2} \qquad \text{ParR)} \frac{p_2 \xrightarrow{\mu} q_2}{p_1 | p_2 \xrightarrow{\mu} p_1 | q_2} \\
 \\
 \text{Rec)} \frac{p[\mathbf{rec} \ x. \ p / x] \xrightarrow{\mu} q}{\mathbf{rec} \ x. \ p \xrightarrow{\mu} q}
 \end{array}$$

# Isomorphic LTS

syntactically different processes  
can exhibit exactly the same behaviour

$\mathbf{nil}$      $\mathbf{nil} \setminus \alpha$      $\mathbf{nil}[\phi]$

$p$      $p + \mathbf{nil}$      $p + p$      $p | \mathbf{nil}$

$p + q$      $q + p$

$p | q$      $q | p$

their LTS are different (states syntactically different)

graph isomorphism abstracts away from states

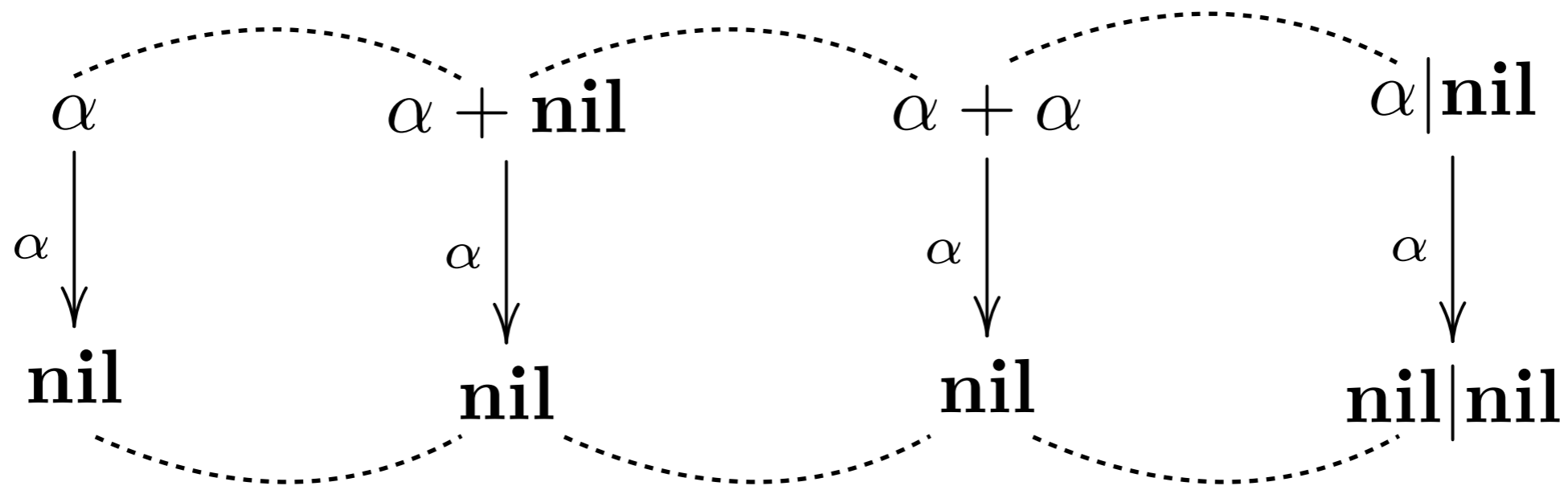
# Graph isomorphism

$$G = (V, E)$$

$$G' = (V', E')$$

$f : V \rightarrow V'$  bijective

$$v \xrightarrow{\mu} w \iff f(v) \xrightarrow{\mu} f(w)$$



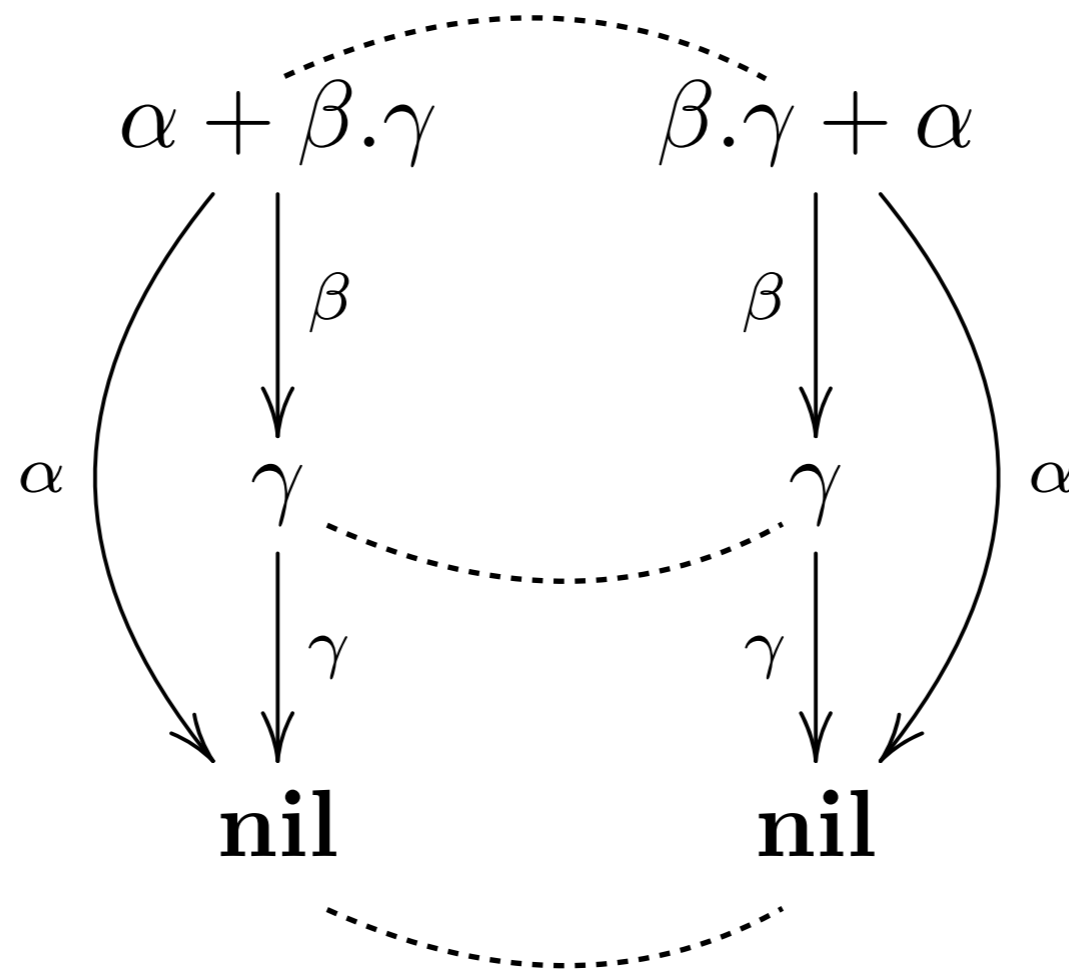
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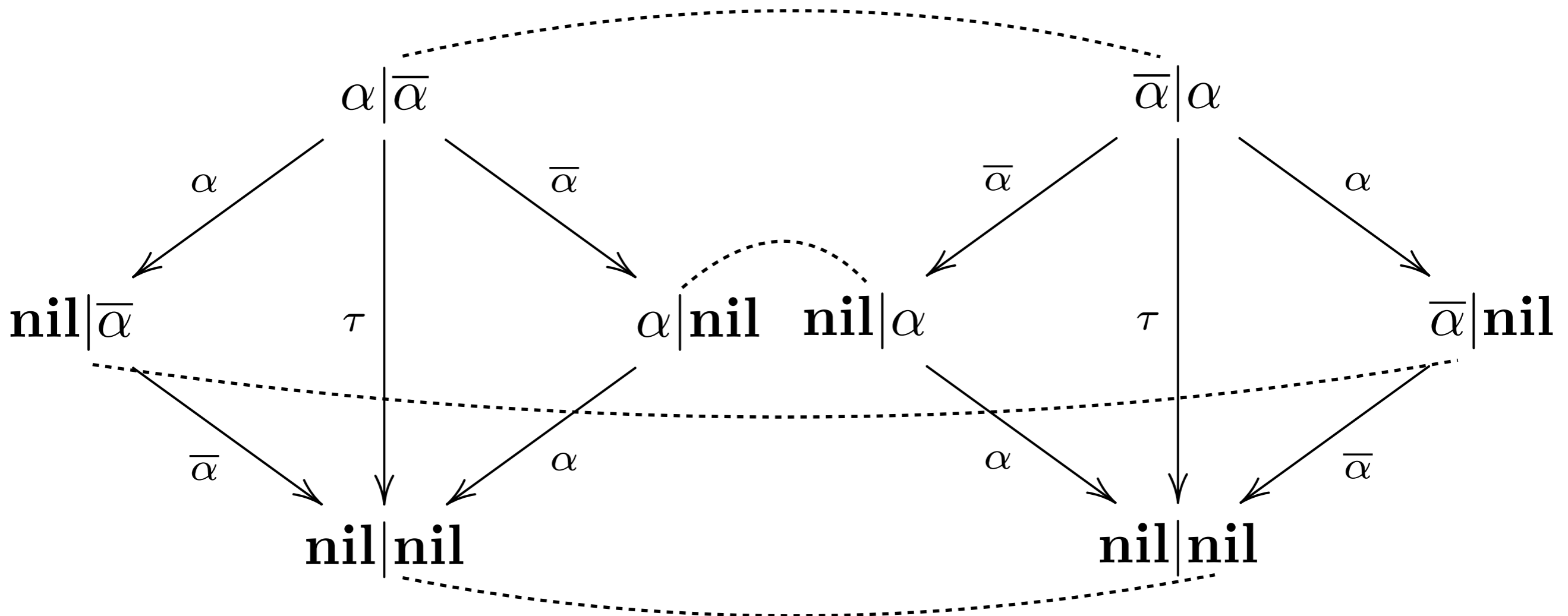
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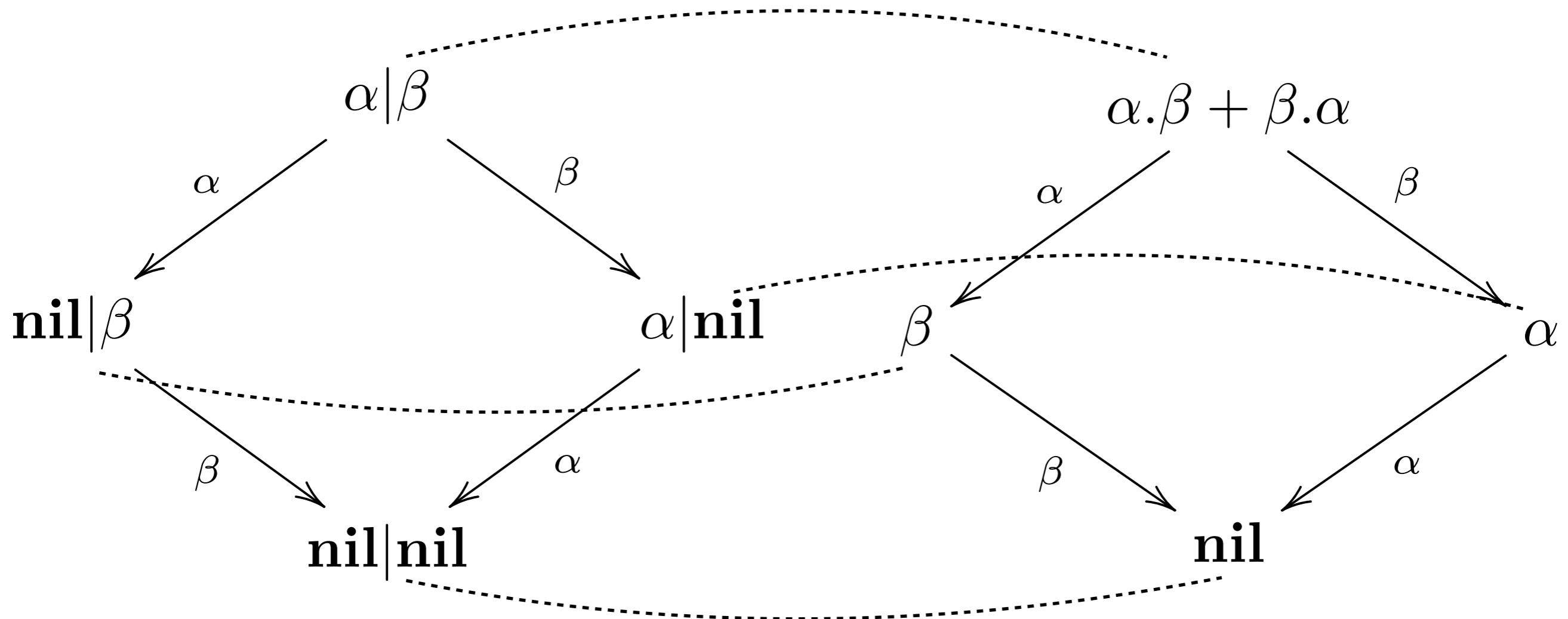
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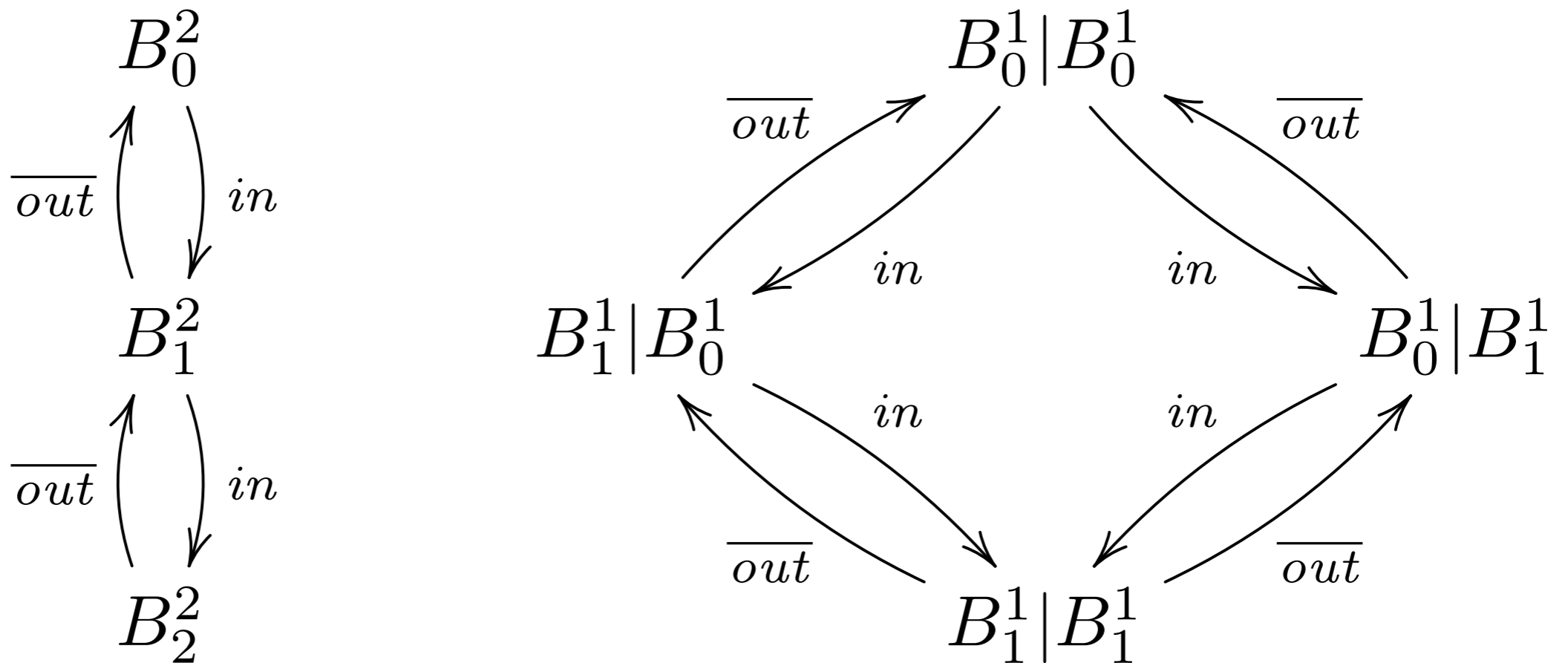
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# Equivalent or not?



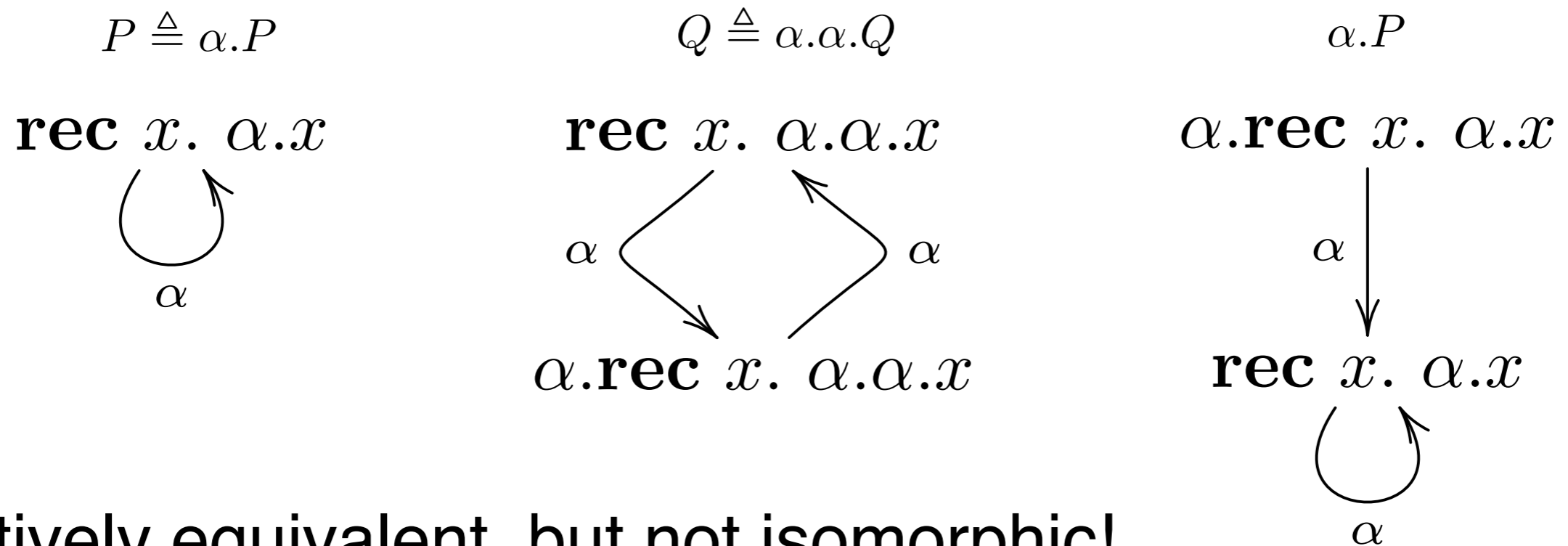
intuitively equivalent, but not isomorphic!

# Iso is too strict

if two processes have isomorphic LTS,  
then they must be considered as equivalent

graph isomorphism captures some interesting equivalences

but is it enough?



intuitively equivalent, but not isomorphic!

# CCS

## Trace equivalence

# Trace equivalence

in automata theory: language equivalence

notion of (finite) trace

$$p \xrightarrow{\mu_1 \mu_2 \cdots \mu_k} q$$

$$p = p_0 \xrightarrow{\mu_1} p_1 \xrightarrow{\mu_2} \cdots \xrightarrow{\mu_k} p_k = q$$

(finite) trace semantics of a process

$$\mathcal{T}(p) = \{ \mu_1 \mu_2 \cdots \mu_k \mid \exists q. p \xrightarrow{\mu_1 \mu_2 \cdots \mu_k} q \}$$

two processes are *trace equivalent*

if they have the same trace semantics

$$p \equiv_{\text{tr}} q \text{ iff } \mathcal{T}(p) = \mathcal{T}(q)$$

is trace equivalent a good notion for concurrent systems?

# Trace equivalence

graph isomorphism implies trace equivalence

we preserves all the useful equivalences seen before

$$p \equiv_{\text{tr}} p + \mathbf{nil} \equiv_{\text{tr}} p + p \equiv_{\text{tr}} p|\mathbf{nil}$$

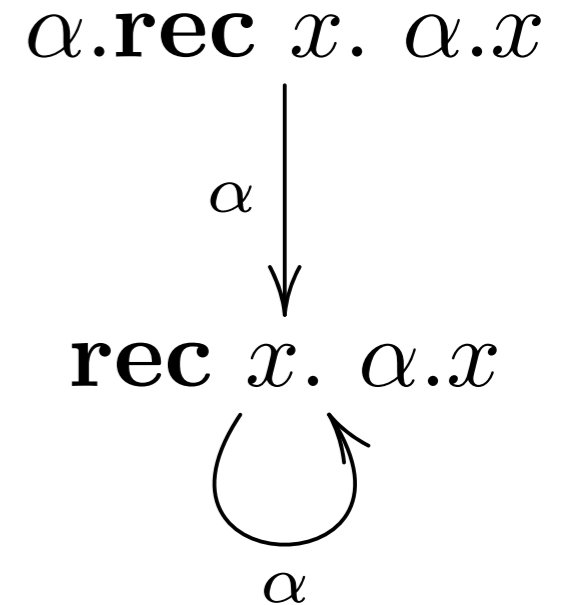
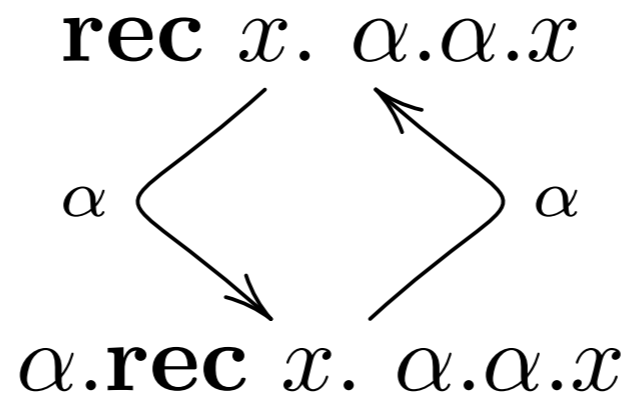
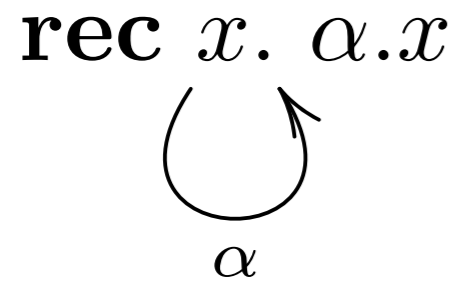
$$p + q \equiv_{\text{tr}} q + p$$

$$p|q \equiv_{\text{tr}} q|p$$

trace semantics is prefix closed

(if a trace is present, all its prefixes are also present)

# Trace equivalence

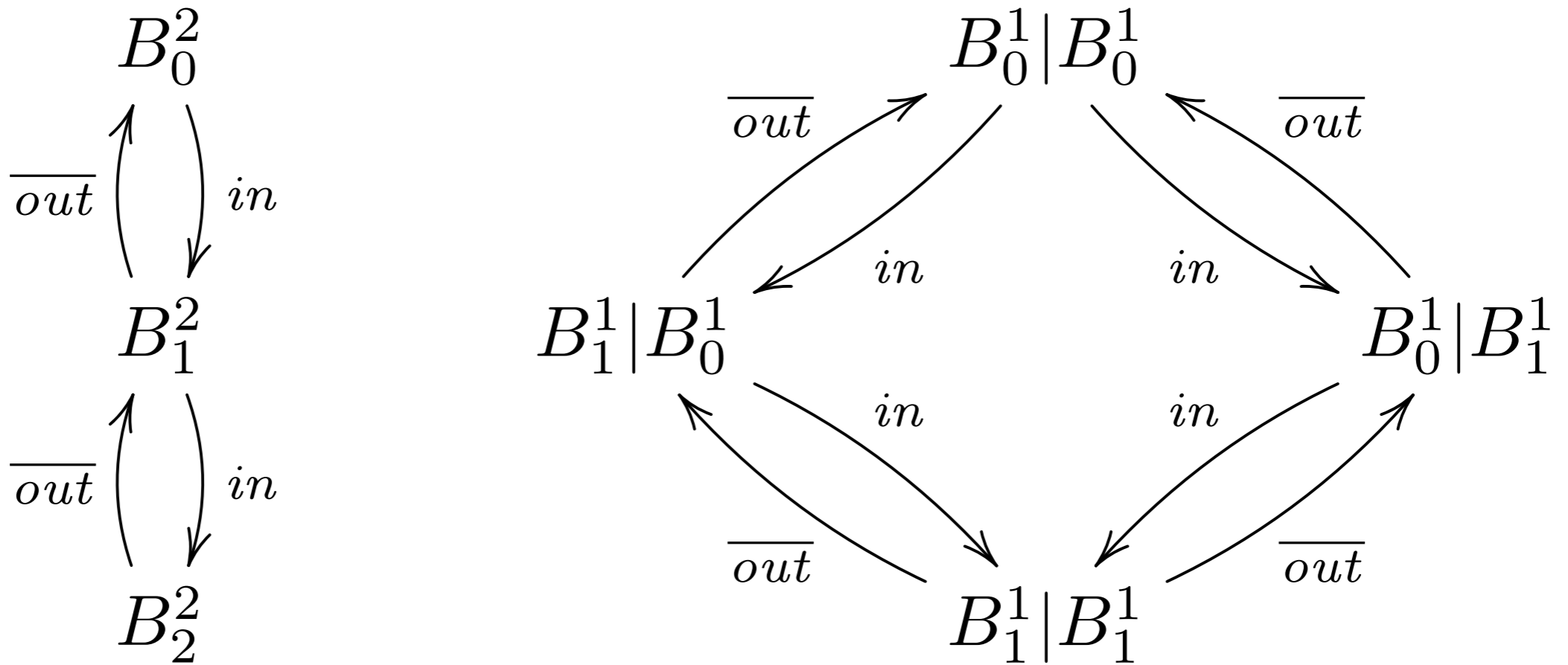


$$\mathcal{T}(\text{rec } x. \alpha.x) = \{\alpha^n \mid n \in \mathbb{N}\}$$

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$$\mathcal{T}(\alpha.\text{rec } x. \alpha.x) = \{\alpha^n \mid n \in \mathbb{N}\}$$

# Trace equivalence



$$\mathcal{T}(B_0^2) = \mathcal{T}(B_0^1 | B_0^1)$$

tentative description:

$0 \leq \#in - \#out \leq 2$  (for any prefix)



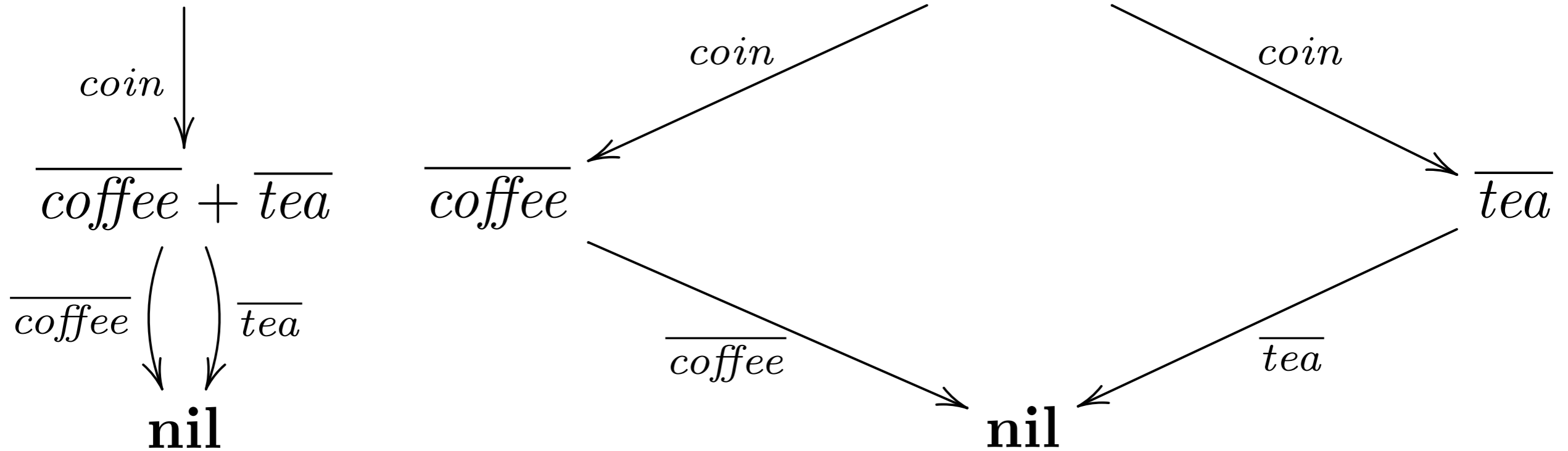
# Two Vending Machines

$coin.(\overline{coffee} + \overline{tea})$

$coin.\overline{coffee} + coin.\overline{tea}$

$coin.(\overline{coffee} + \overline{tea})$

$coin.\overline{coffee} + coin.\overline{tea}$



not isomorphic, but trace equivalent

# Customer's view

which vending machine would you prefer?

$$\text{coin}.\overline{(\text{coffee} + \text{tea})}$$

*coin* ↓

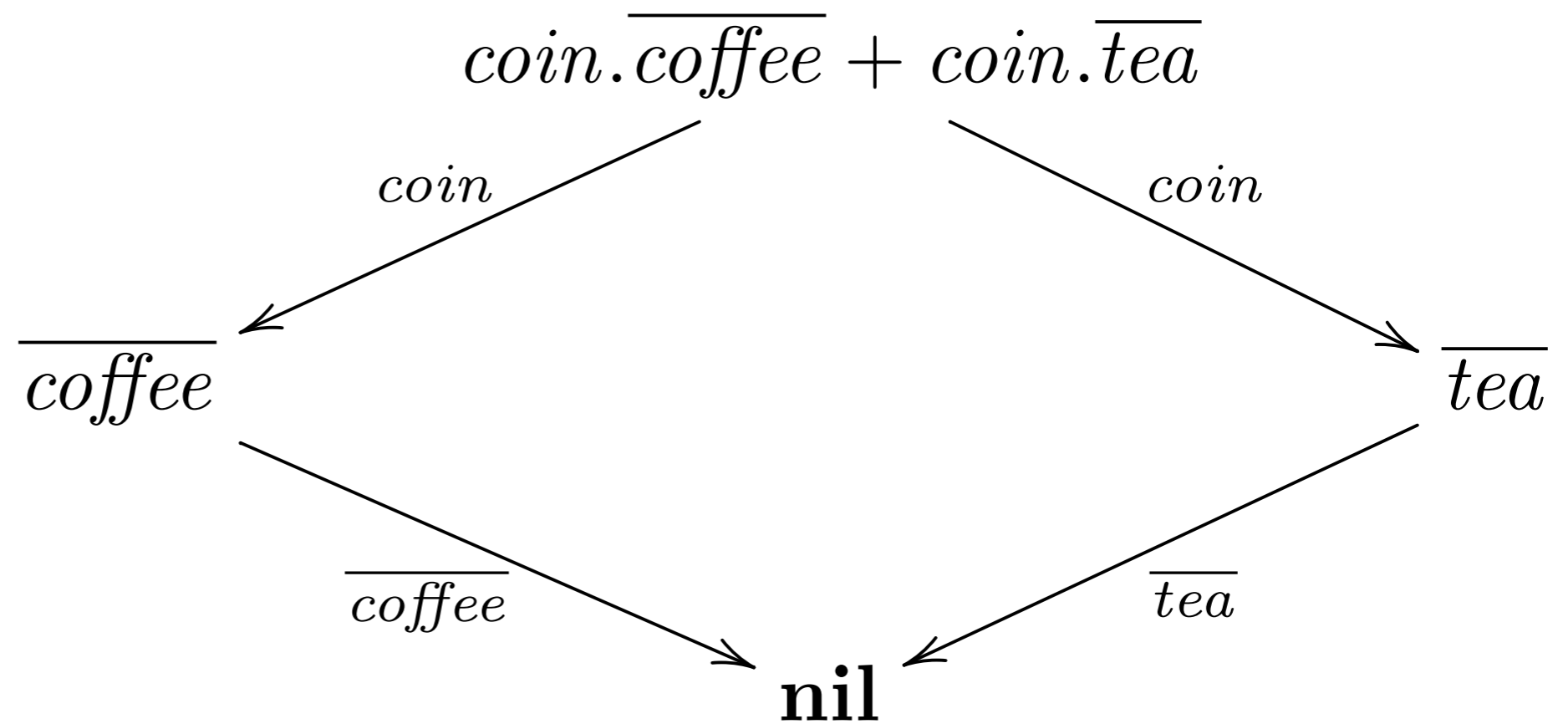
$$\overline{\text{coffee}} + \overline{\text{tea}}$$
$$\overline{\text{coffee}} \quad \left. \begin{array}{l} \text{ ) } \\ \text{ ( } \end{array} \right\} \overline{\text{tea}}$$

**nil**

insert a coin, then choose the drink

# Customer's view

which vending machine would you prefer?

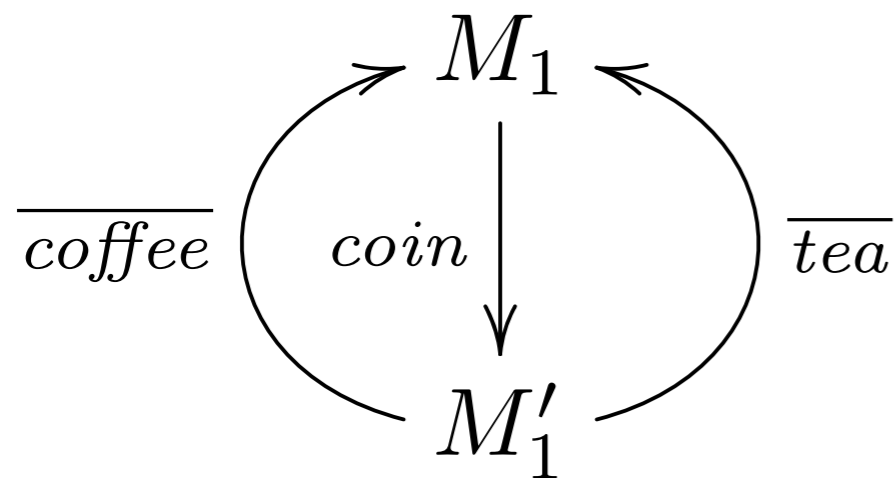


insert a coin, then get the drink chosen by the machine

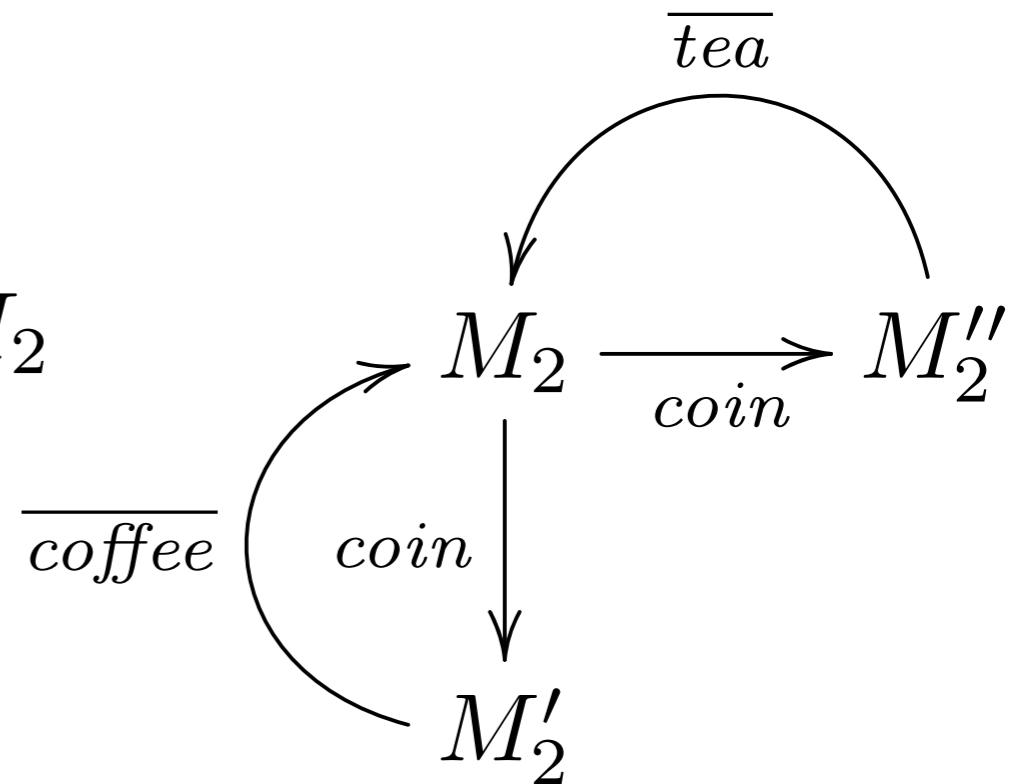
# Recursive Machines

$$M_2 \triangleq \text{coin.} \overbrace{\text{coffee.} M_2}^{M_2'} + \text{coin.} \overbrace{\text{tea.} M_2}^{M_2''}$$

$$M_1 \triangleq \text{coin.} \overbrace{(\text{coffee.} M_1 + \text{tea.} M_1)}^{M_1'}$$



$$M_1 \equiv_{\text{tr}} M_2$$



# System View

$$M_1 \triangleq \text{coin.} \overbrace{(\overline{\text{coffee.}M_1} + \overline{\text{tea.}M_1})}^{M'_1}$$

$$M_1 \equiv_{\text{tr}} M_2$$

$$M_2 \triangleq \text{coin.} \overbrace{\text{coffee.}M_2}^{M'_2} + \text{coin.} \overbrace{\text{tea.}M_2}^{M''_2}$$

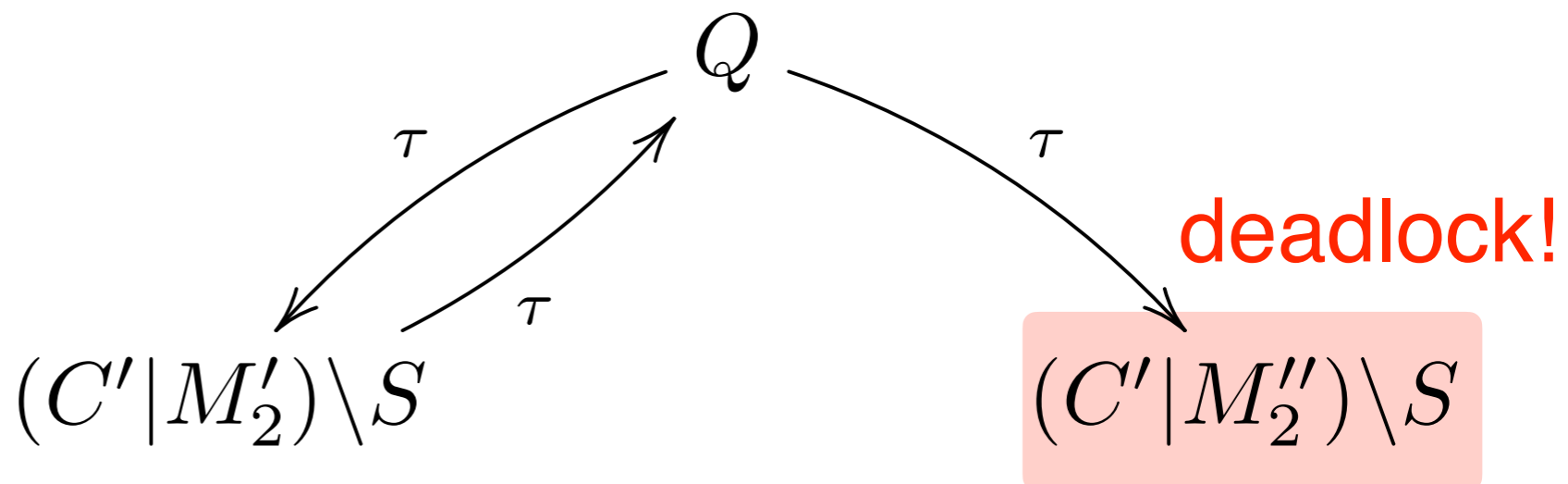
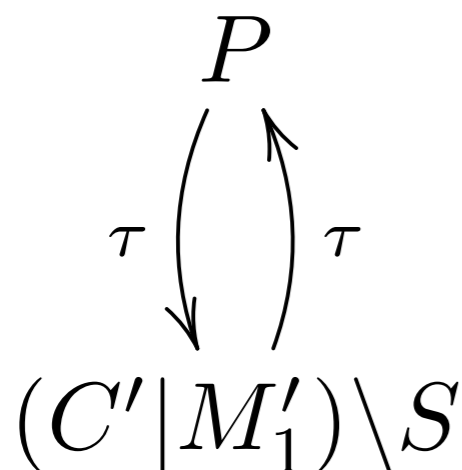
$$C \triangleq \overline{\text{coin.}} \overbrace{\text{coffee.}C}^{C'}$$

$$S \triangleq \{\text{coin, coffee, tea}\}$$

$$P \triangleq (C|M_1) \setminus S$$

$$P \equiv_{\text{tr}} Q$$

$$Q \triangleq (C|M_2) \setminus S$$



# Coming next: bisimilarity

graph isomorphism distinguishes too many processes

trace equivalence identifies too many processes

we need some notion of equivalence in between the two

we introduce the notion of *strong bisimilarity*

as a game

as a fixpoint

as a logical equivalence

to keep in mind: two processes are equivalent unless we have some good reasons to distinguish them